Richard S. Juszkiewicz

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Objective: To obtain a challenging full-time engineering position in the field of digital audio technology that requires creatively solving problems involving DSP hardware/software, spatial audio, CODEC design or audio analysis and synthesis algorithms.

Education:

University of Miami, Coral Gables, Florida (May 2007)
Master of Science, Music Engineering Technology, GPA 3.8

Relevant Coursework:

- Thesis: An Improvement to Anthropometry Based Head and Torso HRTF Synthesis Models for Locations Near the Frontal Median Plane
- Virtual Surround Sound Audio Units Plugin using HRTFs
- Case Studies on IFIR filters and Coefficient Sensitivity in State-Space Realizations
- Discrete Time Speech and Audio Processing (Synthesis and Analysis)
- Electroacoustic and Transducer Design
- VST Plug-in Programming
- Psychoacoustics & Advanced Perceptual Audio CODEC Design
- Filterbanks, Wavelets and Multirate Signal Processing
- Electronic Music Synthesis
- Ambisonics

University at Buffalo, State University of New York (December 2004)
Bachelor of Science, Electrical Engineering with Mathematics Minor
Passed the New York State Fundamentals of Engineering Exam (October 2004)

Honors: Magna Cum Laude, Member of Tau Beta Pi & Eta Kappa Nu Honor Societies

Relevant Coursework:

- Designed, laid out and simulated a 6-bit CMOS ALU
- Designed a 10ms analog peak detector
- Programmed Blackjack in SPIM assembly language on a MIPS processor
- Designed the logic for the circuitry in a vending machine

Activities: Competed on the Track & Field team at the Division I level for four years.

Computer Skills: MATLAB, SPICE3, MAGIC, PSPICE, Cadence, Windows, UNIX, Linux, MacOS X, Microsoft Office, Lotus Notes, SPIM, ARM Assembly, C/C++, Java, Port Audio, VST, AU, Adobe: Photoshop, Audition & After Effects, LaTeX, Macromedia Flash, VDSP++, Reaktor, Reason, ProTools, Digital Performer, Max/MSP

Employment History:

May 2006 – Present Gables Engineering Inc., Coral Gables, FL
DSP & Digital Design Intern

- Successfully designed and prototyped the hardware for the next generation of digital radios using an Analog Devices SHARC processor and interfaced code from previous generation to maintain backwards compatibility
- Gathered performance specifications for aforementioned project using Audio Precision analysis equipment
- Wrote Ethernet drivers on an Analog Devices Blackfin processor for transmission of audio over Ethernet